

Applicant: Paul A. Farrar et al.
Title: BURIED CONDUCTORS

Docket No.: 303.367US2
Filed: August 15, 2001
Examiner: Ha Nguyen

Serial No.: 09/930521
Due Date: January 13, 2003
Group Art Unit: 2812

Box AF
Commissioner for Patents
Washington, D.C. 20231

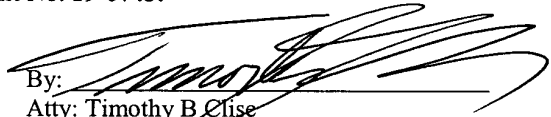


We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response Under 1.116 (8 Pages).
- ☒ Petition for Extension of Time (1 pg.)
- ☒ A Clean Version of Pending Claims (6 pgs.).

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)


By: 
Atty: Timothy B. Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Box AF, Commissioner for Patents, Washington, D.C. 20231, on this 13 day of January, 2003.

Name

Tina Khouat

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EXPEDITED PROCEDURE - EXAMINING GROUP 2812

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul A. Farrar et al.
Serial No.: 09/930,521
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AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

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In response to the Final Office Action mailed September 13, 2002, please amend the application as follows:

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 18, 27, 29-31, 33, 34, 39, 42-45 and 47. The specific amendments to individual claims are detailed in the following marked up set of claims.

18. (Amended) A method comprising:
- forming at least one first trench within a semiconductor substrate at a first depth;
 - depositing a first conductive material, which has a melting point high enough to prevent unwanted metallurgical changes during subsequent processing, substantially at the bottom of each first trench;
 - forming at least one second trench within the semiconductor substrate at a second depth shallower than the first depth;
 - depositing a second conductive material, which has a melting point high enough to prevent unwanted metallurgical changes during subsequent processing, substantially at the bottom of each second trench; and
 - surrounding the first conductive material and the second conductive material with an

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